

U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.		Serial No.	
					M-1007-6C US		08/632,298	
<div style="display: flex; align-items: center;"> <div style="writing-mode: vertical-rl; transform: rotate(180deg); font-size: small; margin-right: 5px;">           59834 U.S. PTO 09/25/97         </div> <div style="border: 1px solid black; padding: 2px;">           INFORMATION DISCLOSURE STATEMENT BY APPLICANT            (Use several sheets if necessary)         </div> </div>					Applicant			
					Amr M. Mohsen			
					Filing Date		Group	
					April 12, 1996		2304 2763	
U.S. Patent Documents								
*Examiner Initial	AA	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA							
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
<div style="font-size: 2em;">✓</div> <div style="font-size: 2em;">✓</div> <div style="font-size: 2em;">✓</div>	AL	0 335 281 A	10/4/89	Europe			<input type="checkbox"/>	<input checked="" type="checkbox"/>
	AM	0 481 703 A	4/22/92	Europe			<input type="checkbox"/>	<input type="checkbox"/>
	AN	0 518 701 A	12/16/92	Europe			<input type="checkbox"/>	<input type="checkbox"/>
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
<div style="font-size: 2em;">✓</div>	AR	Guo et al, "A 1024 Pin Universal Interconnect Array With Routing Architecture", <u>Proceedings of the Custom Integrated Circuits Conference</u> , May 3-6, 1992, pages 4.5.1 - 4.5.4.						
	AS							
	AT							
Examiner		V. TRANS		Date Considered		12/19/97 6/19/99		
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								